

EAST SEARCH

9/2/05

L#	Hits	Search String	Databases	
S1	37055	((integrated or digital) near2 circuit\$1) or processor\$1) with (power near2 (distribution or netw	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S2	1486	S1 and (power near2 network\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S3	66	S2 and (power with (voltage near2 range))	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S4	334	S2 and (network\$1 with response)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S5	7	S2 and (network\$1 with (response near2 function))	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S6	303	S1 and ((power near2 network\$1) with device\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S7	169	S6 and (device\$1 with current)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S8	54	S6 and (device\$1 with current with (amplitude\$1 or value\$1))	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S9	2	S2 and ("shift register" with current with (amplitude\$1 or value\$1))	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S11	35	S1 and (network\$1 with (response near2 function))	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S12	2	S2 and (network\$1 with response with adder)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S13	2	S2 and (power with response with adder)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S14	6	S2 and (response with adder)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S15	6	S2 and (weight\$1 with adder)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S17	45	S2 and ((compar\$4 near2 voltage) with threshold)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S18	24	S2 and ((limit\$3 or adjust\$3 or thrott\$3) near2 (operation\$1 or unit\$1) with voltage)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S19	6	S2 and ((limit\$3 or adjust\$3 or thrott\$3) near2 (operation\$1 or unit\$1) with (threshold or range	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S20	3	S2 and (pipeline with fetch\$3)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S21	24	S2 and (clock near2 gating)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S22	3	S2 and ((clock near2 gating) with instruction\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S23	13	S2 and (inputs with adder)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S24	2	S2 and (recursion near2 coefficient\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S25	2	S2 and ((estimat\$3 near2 voltage\$1) with sequence)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S26	3	S2 and (current near2 coefficient\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S27	8	S2 and (voltage near2 coefficient\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S28	2	S1 and (recursion near2 coefficient\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S29	7	S1 and ((clock near2 gating) with instruction\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S30	44	S1 and (pipeline with fetch\$3)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S31	99	S1 and (current near2 coefficient\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S32	159	S2 and (processor with (execut\$3 near2 instruction\$1))	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S33	4	S2 and ((estimat\$3 near2 voltage\$1) with processor)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S34	5	S2 and ((compar\$4 near2 voltage) with range)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S35	35	S2 and (processor with pipeline\$1)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S36	6	S2 and (processor with pipeline\$1 with (double or multiple))	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S37	14	S2 and ((gating or "gate unit") with current)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB
S38	3	S2 and ((gating or "gate unit") with pipeline)	US-PGPUB; USP	EPO; JPO; DERWENT; IBM_TDB

S39	2	S2 and (entry with "shift register")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S40	16	S1 and (entry with "shift register")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S41	2	S2 and (impulse with (response near2 function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S42	12	S2 and (impulse with function)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S43	136	S2 and (response with function)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S44	301	S3 or S5 or S8 or S9 or S10 or S11 or S12 or S13 or S14 or S15 or S16 or S17 or S18 or S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S45	776	S4 or S6 or S31 or S32 or S43	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S46	147	S44 and S45	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S47	301	S44 or S46	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S16	5	S2 and ((estimat\$3 near2 voltage) with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S48	3917	((integrated or digital) near2 circuit\$1) and (power near2 network)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S49	11	S48 and ((estimat\$3 near2 voltage) with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S50	212	S48 and (voltage with (variation or varies) with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S51	8	S48 and (voltage with (variation or varies) with (comput\$3 or estimat\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S52	7	S48 and (voltage with (variation or varies) with model\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S53	7	S48 and (current with (variation or varies) with model\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S54	7	S2 and ("shift register" with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S10	18	S1 and ("shift register" with current with (amplitude\$1 or value\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S55	12	S2 and (current with (clock adj cycle\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S56	29	S2 and (response near2 function)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S57	7	S56 and (network with (response near2 function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S58	37068	((integrated or digital) near2 circuit\$1) or processor\$1) with (power near2 (distribution or network	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S59	1486	S58 and (power near2 network\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S60	2	S59 and (current with (successive adj cycles\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S61	2	S59 and (current with (successive adj interval\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S62	12	S59 and (current with (clock adj cycle\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S63	4	5,948,106.pn. or "6,304,978".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S64	164	703/18.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S65	54	S64 and (power near2 (network\$1 or circuitry))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S66	40	S65 and (voltage with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S67	25	S66 and (voltage with (threshold or limit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S68	3920	((integrated or digital) near2 circuit\$1) and (power near2 network)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S69	0	S68 and ("successive clocks".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S70	0	S68 and ("successive cycles".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S71	0	S68 and ("successive intervals".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S72	21	S68 and ("clock cycles".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S73	13	S68 and ("current consumption".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S74	10	S68 and ("current amplitudes".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S75	71	S68 and ("current values".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S76	108	S72 or S73 or S74 or S75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S77	29	S76 and (clocks.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S78	34	S76 and (cycles.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S79	21	S76 and (intervals.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

S80

47 S77 or S78 or S79

US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

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9/2/05

Results of search set S47

Document Kind Codes Title	Issue Date	Current OR	Abstract
US 20050182584 A1 Method and system for identifying and locating defects in an integrated circuit	20050818 702/117		
US 20050177150 A1 Electrosurgical generator	20050811 606/34		
US 20050174239 A1 Radio frequency identification tag antenna configurations	20050811 340/572.1		
US 20050169341 A1 Reduced-maintenance excimer laser with oil-free solid state pulser	20050804 372/81		
US 20050152354 A1 Method and systems for ordered dynamic distribution of packet flows over network processing	20050714 370/389		
US 20050149292 A1 Method for detecting communication impulses of a fan motor and circuit arrangement for carryi	20050707 702/183		
US 20050140209 A1 Power distribution web node and power management process	20050630 307/9.1		
US 20050136912 A1 Security and tracking system	20050623 455/423		
US 20050132241 A1 System and method for providing remote monitoring of voltage power transmission and distribi	20050616 713/300		
US 20050114468 A1 Method for configuring a piece of equipment with the use of an associated machine resolvable	20050526 709/217		
US 20050114033 A1 Method and system for transmission of seismic data	20050526 702/14		
US 20050110551 A1 Damping of LC Ringing in IC (Integrated Circuit) Power Distribution Systems	20050526 327/310		
US 20050108582 A1 System, architecture, and method for logical server and other network devices in a dynamically	20050519 713/300		
US 20050090243 A1 System and method for emulating a telephony driver	20050428 455/423		
US 20050086366 A1 Home system including a portable fob having a display	20050421 709/238		
US 20050085248 A1 Home system including a portable fob mating with system components	20050421 455/500		
US 20050085180 A1 Home system including a portable fob having a rotary menu and a display	20050421 455/1		
US 20050081073 A1 Method and apparatus for controlling the power consumption of a semiconductor device	20050414 713/320		
US 20050071106 A1 Expandable intelligent electronic device	20050331 702/104		
US 20050060527 A1 Automatic configuration of equipment software	20050317 713/1		
US 20050040974 A1 Method, system, and apparatus for remote data calibration of a RFID tag population	20050224 341/53		
US 20050027464 A1 Revenue meter with power quality features	20050203 702/61		
US 20050021874 A1 Single chip protocol converter	20050127 709/250		
US 20050001601 A1 Circuit for controlling the minimum operating voltage of switching power supply	20050106 323/282		
US 20040267513 A1 Automatic creation of a design for a high or medium voltage power transmission network	20041230 703/18		
US 20040254750 A1 REVENUE METER BAYONET ASSEMBLY AND METHOD OF ATTACHMENT	20041216 702/61		
US 20040249588 A1 Operation analyzing method for semiconductor integrated circuit device, analyzing system user	20041209 702/66		
US 20040247961 A1 Auxiliary fuel cell system	20041209 429/22		
US 20040243958 A1 Voltage island chip implementation	20041202 716/7		
US 20040238850 A1 Integrated circuit device and method for forming the same	20041202 257/202		
US 20040236972 A1 Firmware controlled dynamic voltage adjustment	20041125 713/300		
US 20040233142 A1 Display device	20041125 345/76		
US 20040225388 A1 Fully digitized audio system	20041111 700/94		

US 20040221185 A1	Processor with low overhead predictive supply voltage gating for leakage power reduction	20041104 713/300
US 20040215750 A1	Configuration program for a security system	20041028 709/220
US 20040212503 A1	Communications architecture for a security network	20041028 340/572.1
US 20040212500 A1	RFID based security network	20041028 340/541
US 20040212497 A1	Multi-controller security network	20041028 340/539.17
US 20040212494 A1	Cordless telephone system	20041028 340/539.1
US 20040212493 A1	RFID reader for a security network	20041028 340/531
US 20040207527 A1	Identification tag utilizing charge pumps for voltage supply generation and data recovery	20041021 340/572.4
US 20040196731 A1	Semiconductor integrated circuit device	20041007 365/233
US 20040196470 A1	Raster image processing (RIP) manager for enhanced print shop operation	20041007 358/1.1
US 20040189271 A1	Power supply system and apparatus	20040930 323/283
US 20040162642 A1	Thin client power management system and method	20040819 700/286
US 20040158663 A1	Interconnect topology for a scalable distributed computer system	20040812 710/100
US 20040151257 A1	Predistortion calibration in a transceiver assembly	20040805 375/296
US 20040141759 A1	System and method for dynamic bandwidth allocation in PONs	20040722 398/168
US 20040136712 A1	Integrated PON processor	20040715 398/60
US 20040136534 A1	System and method for improved data protection in PONs	20040715 380/256
US 20040128625 A1	Resonance reduction arrangements	20040701 716/1
US 20040124715 A1	Apparatus and method for resonance reduction	20040701 307/127
US 20040124713 A1	Distributed power supply arrangement	20040701 307/69
US 20040095193 A1	Radio frequency power amplifier adaptive bias control circuit	20040520 330/285
US 20040095192 A1	Radio frequency power amplifier adaptive bias control circuit	20040520 330/285
US 20040085121 A1	System and method for reducing a leakage current associated with an integrated circuit	20040506 327/544
US 20040060023 A1	Voltage island chip implementation	20040325 716/7
US 20040001326 A1	Power grid and bump pattern with reduced inductance and resistance	20040101 361/775
US 20030236745 A1	Systems and methods for billing in information management environments	20031225 705/40
US 20030202655 A1	Managing power in a line powered network element	20031030 379/413
US 20030200473 A1	System and method for activity or event based dynamic energy conserving server reconfiguration	20031023 713/320
US 20030193245 A1	Device with multiple, concurrently-installed power modules and method for controlling same	20031016 307/64
US 20030188208 A1	System, method, and architecture for dynamic server power management and dynamic workload	20031002 713/320
US 20030146783 A1	Efficient charge pump apparatus	20030807 327/536
US 20030135830 A1	Method of analyzing integrated circuit power distribution in chips containing voltage islands	20030717 716/4
US 20030135351 A1	Functional pipelines	20030717 702/186
US 20030125923 A1	Simulation of di/dt-induced power supply voltage variation	20030703 703/20
US 20030125922 A1	Mechanism for estimating and controlling di/dt-induced power supply voltage variations	20030703 703/18
US 20030117751 A1	Interface device and information processing system	20030626 361/1
US 20030112038 A1	ADAPTING VLSI CLOCKING TO SHORT TERM VOLTAGE TRANSIENTS	20030619 327/100
US 20030101008 A1	Phasor transducer apparatus and system for protection, control, and management of electricity	20030529 702/57
US 20030085621 A1	Power supply methods and configurations	20030508 307/18
US 20030067725 A1	Distributed monitoring and protection system for a distributed power network	20030410 361/65
US 20030065459 A1	Expandable intelligent electronic device	20030403 702/62
US 20030046396 A1	Systems and methods for managing resource utilization in information management environments	20030306 709/226
US 20030043038 A1	Modulating current of an integrated circuit for signal transmission	20030306 340/568.2

US 20030035499 A1	Direct radio frequency (RF) sampling with recursive filtering method	20030220	375/346
US 20030034825 A1	System and method for disconnecting a portion of an integrated circuit	20030220	327/530
US 20030025593 A1	GEOMETRIC HARMONIC MODULATION SIGNALING AND DETECTION	20030206	340/310.01
US 20030021261 A1	Internet telephony gateway system	20030130	370/352
US 20030014200 A1	Revenue meter with power quality features	20030116	702/60
US 20020199160 A1	Power supply network analyzing method, computer program for executing the method, storage	20021226	716/5
US 20020194251 A1	Systems and methods for resource usage accounting in information management environment	20021219	718/105
US 20020190797 A1	Standby circuit for an electrical device	20021219	330/297
US 20020174227 A1	Systems and methods for prioritization in information management environments	20021121	709/226
US 20020171407 A1	Method and apparatus for controlling current demand in an integrated circuit	20021121	323/364
US 20020167405 A1	Radio frequency identification architecture	20021114	340/572.1
US 20020165677 A1	Apparatus and method for seamlessly upgrading the firmware of an intelligent electronic device	20021107	702/62
US 20020162014 A1	Intelligent electronic device with assured data storage on powerdown	20021031	713/200
US 20020158689 A1	CONTROLLER-BASED RADIO FREQUENCY AMPLIFIER MODULE AND METHOD	20021031	330/129
US 20020152305 A1	Systems and methods for resource utilization analysis in information management environment	20021017	709/224
US 20020152044 A1	Method, system, and apparatus for remote timing calibration of a RFID tag population	20021017	702/106
US 20020149483 A1	Method, System, and apparatus for communicating with a RFID tag population	20021017	340/572.1
US 20020149482 A1	Identification tag utilizing charge pumps for voltage supply generation and data recovery	20021017	340/572.1
US 20020149481 A1	Method, system, and apparatus for binary traversal of a tag population	20021017	340/572.1
US 20020149480 A1	Method, system, and apparatus for remote data calibration of a RFID tag population	20021017	340/572.1
US 20020149416 A1	Efficient charge pump apparatus	20021017	327/536
US 20020133593 A1	Systems and methods for the deterministic management of information	20020919	709/226
US 20020120741 A1	Systems and methods for using distributed interconnects in information management environment	20020829	709/225
US 20020112212 A1	Method for supply voltage drop analysis during placement phase of chip design	20020815	716/1
US 20020106090 A1	Reverberation processor based on absorbent all-pass filters	20020808	381/63
US 20020105231 A1	Voltage recovery device for use with a utility power network	20020808	307/64
US 20020104031 A1	Programmable power management system and method	20020801	713/320
US 20020101631 A1	Processing of optical performance data in an optical wavelength division multiplexed communication	20020801	398/1
US 20020097031 A1	Variable power control for process control instruments	20020725	323/273
US 20020095400 A1	Systems and methods for managing differentiated service in information management environment	20020718	707/1
US 20020091869 A1	Service processor and system and method using a service processor	20020711	719/310
US 20020074990 A1	Electric energy service apparatus with tamper detection	20020620	324/110
US 20020065864 A1	Systems and method for resource tracking in information management environments	20020530	718/100
US 20020063635 A1	Point of use digital electric energy apparatus with TCP/IP network communication	20020530	340/870.02
US 20020062454 A1	Dynamic power and workload management for multi-server system	20020523	713/300
US 20020060594 A1	METHOD AND APPARATUS FOR A HIGH FREQUENCY, LOW POWER CLOCK DISTRIBUTION	20020523	327/295
US 20020059274 A1	Systems and methods for configuration of information management systems	20020516	707/100
US 20020049841 A1	Systems and methods for providing differentiated service in information management environment	20020425	709/225
US 20020049608 A1	Systems and methods for providing differentiated business services in information management environment	20020425	705/1
US 20020045993 A1	Point of use digital electric energy apparatus with wireless communication port	20020418	702/62
US 20020045992 A1	Point of use digital electric energy apparatus and method for measuring out-of-specification energy	20020418	702/58
US 20020042684 A1	Point of use digital electric energy apparatus with uninterrupted telephone communication	20020411	702/62
US 20020042683 A1	Point of use digital electric energy apparatus with real-time dual channel metering	20020411	702/61

US 20020007464 A1	Apparatus and method for modular dynamically power managed power supply and cooling sys	20020117 713/320
US 20020004915 A1	System, method, architecture, and computer program product for dynamic power manager	20020110 713/320
US 20020004913 A1	Apparatus, architecture, and method for integrated modular server system providing dynamica	20020110 713/300
US 20020004912 A1	System, architecture, and method for logical server and other network devices in a dynamically	20020110 713/300
US 6934951 B2	Parallel processor with functional pipeline providing programming engines by supporting multipl	20050823 718/107
US 6924991 B2	Energy transfer multiplexer	20050802 363/21.02
US 6906434 B1	Electric utility system with superconducting magnetic energy storage	20050614 307/64
US 6883152 B2	Voltage island chip implementation	20050419 716/5
US 6871150 B2	Expandable intelligent electronic device	20050322 702/62
US 6870410 B1	All digital power supply system and method that provides a substantially constant supply voltag	20050322 327/149
US 6859882 B2	System, method, and architecture for dynamic server power management and dynamic workl	20050222 713/300
US 6844825 B2	Electric energy service apparatus with tamper detection	20050118 340/870.02
US 6820240 B2	Voltage island chip implementation	20041116 716/1
US 6819538 B2	Method and apparatus for controlling current demand in an integrated circuit	20041116 361/90
US 6819180 B2	Radio frequency power amplifier adaptive bias control circuit	20041116 330/285
US 6813571 B2	Apparatus and method for seamlessly upgrading the firmware of an intelligent electronic device	20041102 702/62
US 6792452 B1	Method for configuring a piece of equipment with the use of an associated machine resolvable	20040914 709/217
US 6792364 B2	Revenue meter with power quality features	20040914 702/61
US 6789001 B1	Digital multifunction relay	20040907 700/292
US 6784813 B2	Method, system, and apparatus for remote data calibration of a RFID tag population	20040831 341/53
US 6784568 B2	Device with multiple, concurrently-installed power modules and method for controlling same	20040831 307/66
US 6778534 B1	High-performance network processor	20040817 370/392
US 6766223 B1	Approach for managing power consumption of network devices	20040720 700/286
US 6748572 B2	Power supply network analyzing method, computer program for executing the method, storage	20040608 716/5
US 6745138 B2	Intelligent electronic device with assured data storage on powerdown	20040601 702/61
US 6735706 B2	Programmable power management system and method	20040511 713/300
US 6734797 B2	Identification tag utilizing charge pumps for voltage supply generation and data recovery	20040511 340/572.4
US 6704864 B1	Automatic configuration of equipment software	20040309 713/1
US 6694270 B2	Phasor transducer apparatus and system for protection, control, and management of electricit	20040217 702/57
US 6687627 B1	Electric charge storage apparatus and method for an intelligent electronic device	20040203 702/61
US 6686831 B2	Variable power control for process control instruments	20040203 340/310.01
US 6683494 B2	Digital signal processor enhanced pulse width modulation amplifier	20040127 330/10
US 6657424 B1	DC load detection in an electric utility meter	20031202 324/76.11
US 6650731 B1	Simulator for simulating an intelligent network	20031118 379/15.01
US 6636976 B1	Mechanism to control di/dt for a microprocessor	20031021 713/320
US 6631502 B2	Method of analyzing integrated circuit power distribution in chips containing voltage islands	20031007 716/4
US 6615147 B1	Revenue meter with power quality features	20030902 702/61
US 6594474 B1	Controlled-gain power amplifier device, in particular for radio-frequency circuits applied to cellu	20030715 455/127.2
US 6586971 B1	Adapting VLSI clocking to short term voltage transients	20030701 327/41
US 6586850 B1	Device with multiple, concurrently-installed power molecules and method for controlling same	20030701 307/85
US 6564328 B1	Microprocessor with digital power throttle	20030513 713/320
US 6552709 B1	Power-on display driving method and display driving circuit	20030422 345/99
US 6549867 B1	Power supply feed-forward compensation technique	20030415 702/107

US 6549064 B2	Efficient charge pump apparatus	20030415 327/536
US 6536024 B1	Method for making integrated circuits having gated clock trees	20030318 716/6
US 6523154 B2	Method for supply voltage drop analysis during placement phase of chip design	20030218 716/6
US 6522243 B1	Geometric harmonic modulation signaling and detection	20030218 340/310.01
US 6507243 B2	Controller-based radio frequency amplifier module and method	20030114 330/129
US 6496783 B1	Electric power calculation system	20021217 702/65
US 6496342 B1	Distributed monitoring and protection system for a distributed power network	20021217 361/65
US 6493644 B1	A-base revenue meter with power quality features	20021210 702/61
US 6459175 B1	Universal power supply	20021001 307/149
US 6438678 B1	Apparatus and method for operating on data in a data communications system	20020820 712/34
US 6420935 B1	Controller-based radio frequency amplifier module and method	20020716 330/295
US 6405354 B1	Method and apparatus to optimize power wiring layout and generate wiring layout data for a se	20020611 716/8
US 6373340 B1	High-efficiency audio power amplifier	20020416 330/297
US 6356999 B1	Data processor with trie traversal instruction set extension	20020312 712/209
US 6311147 B1	Integrated circuit power net analysis	20011030 703/18
US 6281725 B1	Semiconductor integrated circuit having a clock recovery circuit	20010828 327/152
US 6278357 B1	Apparatus and method for implementing digital communications on a power line	20010821 340/310.01
US 6247162 B1	Method and apparatus for generating layout data for a semiconductor integrated circuit device	20010612 716/2
US 6243818 B1	Power subsystem for a communication network containing a power bus	20010605 713/300
US 6215261 B1	Application specific integrated circuit for controlling power devices for commutating a motor ba	20010410 318/254
US 6212569 B1	Data processor with bit stuffing instruction set extension	20010403 709/236
US 6211661 B1	Tunable constant current source with temperature and power supply compensation	20010403 323/316
US 6209087 B1	Data processor with multiple compare extension instruction	20010327 712/300
US 6205487 B1	Data processor with bit unstuffing instruction set extension	20010320 709/236
US 6195755 B1	Nonvolatile power management apparatus for integrated circuit application	20010227 713/330
US 6192491 B1	Data processor with CRC instruction set extension	20010220 714/52
US 6175915 B1	Data processor with trie traversal instruction set extension	20010116 712/227
US 6154686 A	Distributed architecture	20001128 700/276
US 6125448 A	Power subsystem for a communication network containing a power bus	20000926 713/300
US 6088288 A	Memory device and method of reducing ground bounce in a memory device	20000711 365/230.06
US 6078222 A	Controller-based radio frequency amplifier module and method	20000620 330/295
US 6065027 A	Data processor with up pointer walk trie traversal instruction set extension	20000516 708/203
US 6047245 A	Resistive strain gauge control circuit	20000404 702/104
US 6043999 A	Apparatus and method for controlling an elevator power supply	20000328 363/89
US 6032253 A	Data processor with multiple compare extension instruction	20000229 712/300
US 6023612 A	Modular transmission system and method	20000208 455/127.1
US 6005759 A	Method and system for monitoring and controlling an electrical distribution network	19991221 361/66
US 5988860 A	System and method for directing air flow having a sash	19991123 700/276
US 5973410 A	Safety device for linking reference grounds in an integrated circuit	19991026 307/10.1
US 5964881 A	System and method to control microprocessor startup to reduce power supply bulk capacitanc	19991012 713/501
US 5963746 A	Fully distributed processing memory element	19991005 712/20
US 5959853 A	Closed-loop switched capacitor network power supply	19990928 363/59
US 5953237 A	Power balancing to reduce step load	19990914 700/295

US 5945713 A	Electrostatic discharge protection circuits for mixed voltage interface and multi-rail disconnect.	19990831 257/355
US 5933358 A	Method and system of performing voltage drop analysis for power supply networks of VLSI circ	19990803 703/14
US 5900179 A	Heating cable control and monitoring method and system	19990504 219/508
US 5892198 A	Method of and apparatus for electronically controlling r.f. energy supplied to a vacuum plasma	19990406 219/121.54
US 5872952 A	Integrated circuit power net analysis through simulation	19990216 703/14
US 5847575 A	Method and apparatus for performing switched supply drive in CMOS pad drivers	19981208 326/27
US 5805146 A	Integrated display screen and slidable control for electrical circuits	19980908 345/172
US 5796631 A	Method and apparatus for monitoring and characterizing power quality, faults and other phenor	19980818 713/340
US 5793194 A	Bias circuit having process variation compensation and power supply variation compensation	19980811 323/312
US 5786709 A	Integrated circuit output driver incorporating power distribution noise suppression circuitry	19980728 326/27
US 5778153 A	Neural network utilizing logarithmic function and method of using same	19980707 706/26
US 5740031 A	Control system for the irrigation of watering stations	19980414 700/16
US 5723848 A	Heating cable control and monitoring method and system	19980303 219/508
US 5717943 A	Advanced parallel array processor (APAP)	19980210 712/20
US 5692160 A	Temperature, process and voltage variant slew rate based power usage simulation and metho	19971125 703/23
US 5687068 A	Power supply for in-line power controllers and two-terminal electronic thermostat employing sai	19971111 363/126
US 5670864 A	Adaptive automatic power capacitor for controlling controller a capacitor bank of a power distrib	19970923 323/211
US 5648910 A	Method of automatically optimizing power supply network for semi-custom made integrated cir	19970715 716/2
US 5642249 A	Method and apparatus for limiting high current electrical faults in distribution networks by use of	19970624 361/58
US 5642002 A	Apparatus and methods for generating uninterruptible AC power signals	19970624 307/64
US 5638244 A	Apparatus and methods for generating uninterruptible AC power signals	19970610 361/62
US 5625803 A	Slew rate based power usage simulation and method	19970429 703/14
US 5621720 A	Multichannel alignment system and method	19970415 370/241
US 5594908 A	Computer system having a serial keyboard, a serial display, and a dynamic memory with mem	19970114 710/67
US 5592131 A	System and method for modulating a carrier frequency	19970107 332/103
US 5590179 A	Remote automatic meter reading apparatus	19961231 379/106.06
US 5581661 A	Artificial neuron using adder circuit and method of using same	19961203 706/43
US 5553056 A	Packetized remote test system for a telephone network	19960903 370/253
US 5548685 A	Artificial neuron using adder circuit and method of using same	19960820 706/43
US 5488306 A	Open and short fault detector for a differential interface	19960130 324/539
US 5469365 A	Power monitor unit	19951121 702/62
US 5448222 A	Coupled transmission line sensor cable and method	19950905 340/566
US 5446446 A	Differential, multiple cell reflex cable intrusion detection system and method	19950829 340/566
US 5438528 A	Method and apparatus for testing an interactive network board in a local area network (LAN).	19950801 702/119
US 5414614 A	Dynamically configurable switched capacitor power supply and method	19950509 363/59
US 5396444 A	Terminator keys having reciprocal exponents in a data processing system	19950307 708/145
US 5387189 A	Electrotransport delivery device and method of making same	19950207 604/20
US 5351177 A	Switch mode power supply with standby mode operation	19940927 363/21.09
US 5349542 A	Method for sizing widths of power busses in integrated circuits	19940920 703/15
US 5260548 A	Soldering system controlled power supply apparatus and method of using same	19931109 219/483
US 5224029 A	Power factor and harmonic correction circuit including ac startup circuit	19930629 363/48
US 5148167 A	Sigma-delta oversampled analog-to-digital converter network with chopper stabilization	19920915 341/143
US 5148166 A	Third order sigma delta oversampled analog-to-digital converter network with low component s	19920915 341/143

US 5142286 A	Read-out photodiodes using sigma-delta oversampled analog-to-digital converters	19920825 341/143
US 5065157 A	High order sigma delta oversampled analog-to-digital converter integrated circuit network with i	19911112 341/143
US 5050107 A	Side-by-side displays for instrument having a data processing system	19910917 358/1.3
US 5032972 A	Method and circuit configuration for increasing the efficiency of resonant converter power suppl	19910716 363/97
US 4962341 A	Low voltage non-saturating logic circuit technology	19901009 326/43
US 4845466 A	System for high speed digital transmission in repetitive noise environment	19890704 340/310.03
US 4749947 A	Grid-based, "cross-check" test structure for testing integrated circuits	19880607 714/734
US 4669051 A	Vector network analyzer with integral processor	19870526 702/191
US 4661767 A	Vector network analyzer with integral processor	19870428 324/638
US 4644486 A	Vector network analyzer with integral processor	19870217 702/57
US 4641086 A	Vector network analyzer with integral processor	19870203 324/76.22
US 4641085 A	Vector network analyzer with integral processor	19870203 324/76.22
US 4636717 A	Vector network analyzer with integral processor	19870113 324/76.22
US 4594007 A	Time-programming apparatus	19860610 368/10
US 4593361 A	Vending machine control circuit	19860603 700/238
US 4589051 A	Second breakdown protection circuit for X-ray generator inverter	19860513 363/56.05
US 4539618 A	Digitally controlled overload relay	19850903 361/94
US 4495446 A	Lighting unit with improved control sequence	19850122 315/206
US 4481590 A	Vending machine control circuit	19841106 700/244
US 4471268 A	Lighting unit having power supply with improved switching means	19840911 315/178
US 4459651 A	Regulated flyback power supply using a combination of frequency and pulse width modulation	19840710 363/21.18
US 4456871 A	Power supply for electronic control system	19840626 323/217
US 4455679 A	Scanning radio receiver and frequency synthesizer therefor	19840619 455/165.1
US 4445776 A	High registration photomask machine and computerized numerical control system	19840501 355/78
US 4422180 A	Control signal transmitting apparatus, particularly for aircraft	19831220 398/110
US 4398304 A	Scanning radio receiver including detector circuit for AM and FM reception	19830809 455/168.1
US 4372464 A	Vending machine control circuit	19830208 221/14
US 4364110 A	Computerized machine control system	19821214 700/180
US 4345145 A	User programmable control system for toaster oven appliance	19820817 219/492
US 4310805 A	Phase-locked loop stabilized by a crystal oscillator	19820112 331/1A
US 4290538 A	Fuel-dispensing system with self-checking means	19810922 222/25
US 4276590 A	Current sharing modular power system	19810630 363/71
US 4270217 A	Processor controlled, service selecting, AM/FM scanning radio receiver	19810526 455/165.1
US 4242595 A	Tunnel diode load for ultra-fast low power switching circuits	19801230 326/132
US 4225225 A	High registration photomask machine and computerized numerical control system	19800930 355/78
US 4218625 A	Synchronizing check relay	19800819 307/87
US 4198563 A	Photodetector timer network	19800415 250/214AL
US 4131942 A	Non-volatile storage module for a controller	19781226 714/22
US 4120583 A	High registration photomask method and apparatus	19781017 355/86
US 4114099 A	Ultrasonic television remote control system	19780912 348/734
US 4065679 A	Dynamic logic system	19771227 326/98
US 3983380 A	Auxiliary memory unit for use with an electronic display calculator	19760928 708/164
US 3974472 A	Domestic appliance control and display panel	19760810 345/211

US 3972320 A	Patient monitoring system	19760803 600/519
US 3916218 A	Integrated power supply for merged transistor logic circuit	19751028 326/100
US 3911415 A	Distribution network power line carrier communication system	19751007 340/310.03
US 3790816 A	ENERGY STORAGE AND TRANSFER POWER PROCESSOR WITH SELF-CONTAINED PA	19740205 307/149
US 3748492 A	LIGHT-TRIGGERED ELECTRIC POWER SOURCE	19730724 307/117
US 3732728 A	BOTTOM HOLE PRESSURE AND TEMPERATURE INDICATOR	19730515 340/854.4
US 3708685 A	HIGH INDUCTIVE LOAD ENERGIZING CIRCUIT	19730102 307/104
US 3678405 A	AMPLIFIER-LIMITER CIRCUIT WITH REDUCED AM TO PM CONVERSION	19720718 330/260
US 20020199160 A	Power supply network analyzing method of large-scale semiconductor integrated circuit device	20021226
US 6195755 B	Power management apparatus e.g. for CMOS integrated circuits, has switching unit through wh	20010227

10/040582

Edward Grochosowski et al.

EAST SEARCH

9/2/05

L#	Hits	Search String	Databases
S68	3920	((integrated or digital) near2 circuit\$1) and (power near2 network)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S69	0	S68 and ("successive clocks".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S70	0	S68 and ("successive cycles".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S71	0	S68 and ("successive intervals".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S72	21	S68 and ("clock cycles".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S73	13	S68 and ("current consumption".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S74	10	S68 and ("current amplitudes".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S75	71	S68 and ("current values".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S76	108	S72 or S73 or S74 or S75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S77	29	S76 and (clocks.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S78	34	S76 and (cycles.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S79	21	S76 and (intervals.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S80	47	S77 or S78 or S79	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

10/040582

Edward Grochosowski et al.

EAST SEARCH

9/2/05

Results of search set S47

Document	Kind	Codes	Title	Issue Date	Current OR	Abstract
US 20050135429	A1		Time and data synchronization between network devices	20050623	370/503	
US 20050108667	A1		METHOD FOR DESIGNING AN INTEGRATED CIRCUIT HAVING MULTIPLE VOLTAGE DOMAINS	20050519	716/4	
US 20050062625	A1		Arrangement for generating a clock signal for a sigma-delta analog-to-digital converter	20050324	341/123	
US 20050050502	A1		Method and apparatus for designing semiconductor integrated circuit	20050303	716/10	
US 20040189271	A1		Power supply system and apparatus	20040930	323/283	
US 20040128115	A1		Hierarchical power supply noise monitoring device and system for very large scale integrated circuit	20040701	702/191	
US 20040039535	A1		Repair of address-specific leakage	20040226	702/57	
US 20030125923	A1		Simulation of di/dt-induced power supply voltage variation	20030703	703/20	
US 20030125922	A1		Mechanism for estimating and controlling di/dt-induced power supply voltage variations	20030703	703/18	
US 20030033580	A1		Method and apparatus for reducing power consumption in VLSI circuit designs	20030213	716/2	
US 20020116560	A1		Method and apparatus for distributing multi-source/multi-sink control signals among nodes on a network	20020822	710/100	
US 20020059040	A1		Network diagnostic meter	20020516	702/120	
US 20020042683	A1		Point of use digital electric energy apparatus with real-time dual channel metering	20020411	702/61	
US 6937971	B1		System and method for determining the desired decoupling components for a power distribution network	20050830	703/18	

US 6891389 B1	System and method for detecting quiescent current in an integrated circuit	20050510 324/763
US 6823293 B2	Hierarchical power supply noise monitoring device and system for very large scale integrated circuit	20041123 702/191
US 6754748 B2	Method and apparatus for distributing multi-source/multi-sink control signals among nodes on a network	20040622 710/100
US 6711719 B2	Method and apparatus for reducing power consumption in VLSI circuit designs	20040323 716/2
US 6636976 B1	Mechanism to control di/dt for a microprocessor	20031021 713/320
US 6586971 B1	Adapting VLSI clocking to short term voltage transients	20030701 327/41
US 6526362 B2	Network diagnostic meter	20030225 702/82
US 6502044 B1	Self-diagnostic circuitry for emergency lighting fixtures	20021231 702/63
US 6480989 B2	Integrated circuit design incorporating a power mesh	20021112 716/8
US 6212049 B1	Load center monitor for electrical power lines	20010403 361/64
US 6107835 A	Method and apparatus for a logic circuit with constant power consumption	20000822 326/98
US 5963746 A	Fully distributed processing memory element	19991005 712/20
US 5933358 A	Method and system of performing voltage drop analysis for power supply networks of VLSI circuit	19990803 703/14
US 5834943 A	Apparatus and method for sensing failed temperature responsive sensors	19981110 324/549
US 5798932 A	Methods and apparatus for removing error due to decaying offsets from measured power system	19980825 702/59
US 5784233 A	Differential protection device of a power transformer	19980721 361/36
US 5737231 A	Metering unit with enhanced DMA transfer	19980407 702/61
US 5717943 A	Advanced parallel array processor (APAP)	19980210 712/20
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US 5625836 A	SIMD/MIMD processing memory element (PME)	19970429 709/214
US 5410263 A	Delay line loop for on-chip clock synthesis with zero skew and 50% duty cycle	19950425 327/141
US 5404310 A	Method and apparatus for power-source wiring design of semiconductor integrated circuits	19950404 716/13
US 539974 A	Fault current filter	19950321 324/521
US 5373411 A	Remote control circuit breaker system	19941213 361/64
US 5237511 A	Distribution automation smart remote terminal unit	19930817 702/58
US 5086491 A	Electronic control circuit for A/C motors	19920204 388/811
US 5059896 A	Electronic watt-hour meter	19911022 324/142
US 5008664 A	Apparatus for inductively coupling signals between a downhole sensor and the surface	19910416 340/854.8
US 4978911 A	Electrical energy analyzer	19901218 324/142
US 4845466 A	System for high speed digital transmission in repetitive noise environment	19890704 340/310.03
US 4723220 A	Apparatus for power measuring and calculating Fourier components of power line parameters	19880202 702/62
US 4718084 A	Overload protection circuit for a telephone subscriber line feed circuit	19880105 379/412
US 4584569 A	Motion sensitive security system	19860422 340/566